

**KAKATIYA INSTITUTE OF TECHNOLOGY & SCIENCE,
WARANGAL - 506 015**

II Mid of I Year M.Tech. Examination - January, 2014

Time: 10-30 AM to 12-30 Noon

DATE & DAY	Structural & Construction Engg.	Design Engineering	Digital Communication	Software Engineering	VLSI & Embedded Systems	Power Electronics
06-01-2014 [Monday]	Numerical and Statistical Methods	Optimization Methods in Engg. Design	Detection & Estimation Theory	Discrete Mathematics & Optimization Techniques	Optimization Techniques & Graph Theory	Analysis of power electronic converters
07-01-2014 [Tuesday]	Limit Analysis of Concrete structures	Fundamental Principles of Engg. Design	Data & Computer Communication (DCC)	Object Oriented Software Engineering	Digital Design	Energy conversion systems
08-01-2014 [Wednesday]	Advanced Concrete Technology	Stress Analysis	Advanced Digital Signal Processing	Software Project Management	Analog Design	Machine modeling & analysis
09-01-2014 [Thursday]	Advanced Analysis of Structures	Mechanical Vibrations	Microwave & Optical Fiber Communication System	Advanced Operating System	VLSI Technology	Power electronics control of D.C. drives
10-01-2014 [Friday]	Construction Techniques and Equipment	Computer Aided Design & Graphics	Data Compression Techniques	Advanced Computer Architecture	Embedded Systems Concepts	Modern control theory
11-01-2014 [Saturday]	<u>ELECTIVE-I</u> Total Quality Management	<u>ELECTIVE -I</u> Smart Structures	<u>ELECTIVE -I</u> Artificial Neural Networks	<u>ELECTIVE-I</u> Data Structures & Algorithms	<u>ELECTIVE -I</u> Data Communications & Computer Networks	HVDC Transmission

M. Lakshminarayana
Dean, Evaluation

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