

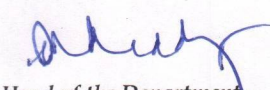
ACADEMIC YEAR 2026-27: ODD SEMESTER	3ECE1
Scheme: URR24-R25 III SEMESTER	B-VI-101
B.Tech Electronics & Communication Engineering (ECE)	W.e.f. 20.07.2026

Class Teacher: Sri. B. Komuraiah (BK)

DAY / TIME	9.30 - 10.30	10.30 - 11.30	11.30 - 12.30	12.30 - 01.30	L U N C H B R E A K	14.30 - 15.30	15.30 - 16.30
MONDAY	SS	OOP	DD	ECAD		B1: DD LAB (RS+ESU) B1 : 202/ B2: ECAD LAB (BK+PRR) B1 : 206	
TUESDAY	AM	SS	OOP	ECAD(T)		PSD LAB (DV+BRD) B6 : 104 & 105	
WEDNESDAY	DD	AM	SS	MYC		B1: ECAD LAB (BK + PRR) B1 : 206/ B2: DD LAB (RS + ESU) B1 : 202	
THURSDAY	ECAD	OOP	SS (T)	ECA		DD	AM (T)
FRIDAY	OOP LAB (JTK+KS+ES+AV) B6 : 104 & 105		AM	DD (T)		ECAD	OOP(T)
SATURDAY							

Course Title	Course Code & Course Name	Course Faculty
AM	U24MH301H Applied Mathematics	Dr.V. Anand (VA)
ECAD	U24EC302 Electronic Circuits Analysis and Design	Sri. B. Komuraiah (BK)
DD	U24EC303 Digital Design	Dr. R. Srikanth (RS)
SS	U24EC304 Signals and Systems	Smt. A. Vijaya (AV)
OOP	U24EC305 OOP through Java	Dr. J. Tarun Kumar (JTK)
ECAD LAB	U24EC306 Electronic Circuits Analysis and Design Laboratory	B1 & B2: Sri. B. Komuraiah (BK)/ Sri. P. Ramchandrarao (PRR)
PSD LAB	U24SE307 Programming Skill Development Lab - 2	Dr. D. Venu (DV)/ Dr. B. Ramadevi (BRD)
DD LAB	U24EC308 Digital Design Laboratory	B1 & B2:: Dr. R. Srikanth (RS) / Smt. E. Susmitha (ESU)
OOP LAB	U24EC309 OOP through Java Laboratory	Dr. J. Tarun Kumar (JTK) / Sri. E. Suresh (ES)/ Dr. K. Sowjanya (KS)/ Smt. A. Vijaya (AV)
ECA	ECE Association	Sri A. Pavan (AP) / Dr. K. Sowjanya (KS) / Dr. MD. Abdul Muqueem (MQ) / Smt. E. Susmitha (ESU)
MYC	Meet Your Counsellor	


 Faculty I/c,
 Dept. Timetables


 Head of the Department
 20/07/26


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 Overall Timetables


 Dean, AA

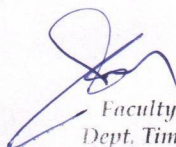

 Principal

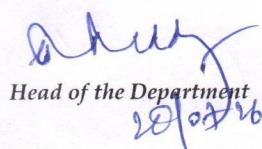
ACADEMIC YEAR 2026-27: ODD SEMESTER	3ECE2
Scheme: URR24-R25 III SEMESTER	B-VI-102
B.Tech Electronics & Communication Engineering (ECE)	w.e.f.20.07.2026

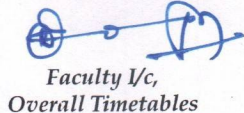
Class Teacher: Smt. E. Susmitha (ESU)

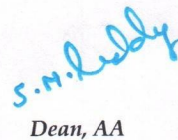
DAY / TIME	9.30 - 10.30	10.30 - 11.30	11.30 - 12.30	12.30 - 01.30		14.30 - 15.30	15.30 - 16.30
MONDAY	DD	SS	DD (T)	ECAD	L U N C H B R E A K	AM	OOP (T)
TUESDAY	ECAD	AM	DD	ECAD (T)		OOP	SS
WEDNESDAY	OOP LAB (KS+ ES+JTK+VR) B6 : 104 & 105		SS	MYC		OOP	AM (T)
THURSDAY	PSD LAB (KS+GRR) B6 : 104 & 105		AM	ECA		B1: DD LAB (ESU+AP) B1 : 202/ B2: ECAD LAB (VVR+BK) B1 : 206	
FRIDAY	B2: DD LAB (ESU+AP) B1: 202/ B1: ECAD LAB (VVR+BK) B1 : 206		OOP	ECAD		DD	SS (T)
SATURDAY							

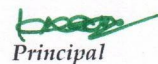
Course Title	Course Code & Course Name	Course Faculty
AM	U24MH301H Applied Mathematics	Dr.S. Vishwa Prasad Rao (VPR)
ECAD	U24EC302 Electronic Circuits Analysis and Design	Dr. V. Venkateshwar Reddy (VVR)
DD	U24EC303 Digital Design	Smt. E. Susmitha (ESU)
SS	U24EC304 Signals and Systems	Dr. J. Sheshagiri Babu (JSB)
OOP	U24EC305 OOP through Java	Dr. K. Sowjanya (KS)
ECAD LAB	U24EC306 Electronic Circuits Analysis and Design Laboratory	B1&B2: Dr. V. Venkateshwar Reddy (VVR) / Sri. B. Komuraiah (BK)
PSD LAB	U24SE307 Programming Skill Development Lab - 2	Dr. K. Sowjanya (KS)/ Dr. G. Raghotham Reddy (GRR)
DD LAB	U24EC308 Digital Design Laboratory	B1&B2: Smt. E. Susmitha (ESU) / Sri. A. Pavan (AP)
OOP LAB	U24EC309 OOP through Java Laboratory	Dr. K. Sowjanya (KS)/ Sri. E. Suresh (ES)/ Dr. J. Tarun Kumar (JTK)/ Dr. V. Raju (VR)
ECA	ECE Association	Sri A. Pavan (AP) / Dr. K. Sowjanya (KS) / Dr. MD. Abdul Muqueem (MQ) / Smt. E. Susmitha (ESU)
MYC	Meet Your Counsellor	


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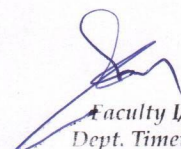

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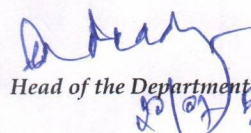
ACADEMIC YEAR 2026-27: ODD SEMESTER	3ECE3
Scheme: URR24-R25 III SEMESTER	B-VI-103
B.Tech Electronics & Communication Engineering (ECE)	w.e.f.20.07.2026

Class Teacher: Dr. M. Chandrashekar (MC)

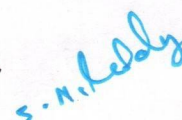
DAY / TIME	9.30 - 10.30	10.30 - 11.30	11.30 - 12.30	12.30 - 01.30	L U N C H B R E A K	14.30 - 15.30	15.30 - 16.30
MONDAY	AM	SS	B1: DD LAB (AV + BD) B1:202/ B2: ECAD LAB (MC + SUM) B1 : 206			ECAD	OOP
TUESDAY	OOP LAB (ES+JTK+KS+EJ) B6 : 104 & 105		AM	DD		ECAD	OOP (T)
WEDNESDAY	DD	SS	OOP	MYC		PSD LAB (MC+SPG) B6 : 104 & 105	
THURSDAY	DD	SS	OOP	ECA		AM	ECAD (T)
FRIDAY	ECAD	SS (T)	B2: DD LAB (AV + BD) B1 : 202/ B1: ECAD LAB (MC + SUM) B1 : 206			AM (T)	DD (T)
SATURDAY							

Course Title	Course Code & Course Name	Course Faculty
AM	U24MH301H Applied Mathematics	Dr. Narahari Raji Reddy (NRR)
ECAD	U24EC302 Electronic Circuits Analysis and Design	Dr. M. Chandrashekar (MC)
DD	U24EC303 Digital Design	Smt. A. Vijaya (AV)
SS	U24EC304 Signals and Systems	Dr. K. Ashoka Reddy (KAR)
OOP	U24EC305 OOP through Java	Sri. E. Suresh (ES)
ECAD LAB	U24EC306 Electronic Circuits Analysis and Design Laboratory	B1 & B2: Dr. M. Chandrashekar (MC) / Dr. S. Umamaheshwar (SUM)
PSD LAB	U24SE307 Programming Skill Development Lab - 2	Dr. M. Chandrashekar (MC) / Dr. S. P. Girija (SPG)
DD LAB	U24EC308 Digital Design Laboratory	B1 & B2: Smt. A. Vijaya (AV) / Dr. B. Dhanalaxmi (BD)
OOP LAB	U24EC309 OOP through Java Laboratory	Sri. E. Suresh (ES) / Dr. K. Sowjanya (KS)/ Dr. J. Tarun Kumar (JTK)/ Ms. E. Jawali (EJ)
ECA	ECE Association	Sri A. Pavan (AP) / Dr. K. Sowjanya (KS) / Dr. MD. Abdul Muqueem (MQ) / Smt. E. Susmitha (ESU)
MYC	Meet Your Counsellor	


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 Dean, AA

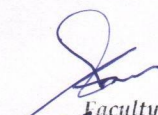

 Principal

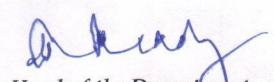
ACADEMIC YEAR 2026-27: ODD SEMESTER	5ECE1
Scheme: URR24 V SEMESTER	B-VI-201
B.Tech Electronics & Communication Engineering (ECE)	w.e.f.20.07.2026

Class Teacher: Sri. D. Santhosh Kumar (DSK)

DAY / TIME	9.30 - 10.30	10.30 - 11.30	11.30 - 12.30	12.30 - 01.30	L U N C H B R E A K	14.30 - 15.30	15.30 - 16.30
MONDAY	B1: MCA LAB (DSK+MQ) B1 : 204 B2: AIML LAB (BRD + RS) B6 : 104	LCS	DT			MCA	VLSID (T)
TUESDAY	LCS	AIML	MCA	DT (T)		TECHNICAL ENGLISH (B IV: 308 & 309)	
WEDNESDAY	B2: MCA LAB (DSK + MQ) B1 : 204 B1: VLSID LAB (TSK+VSR) B6 : 208		EITK	MYC		LCS (T)	AIML (T)
THURSDAY	EITK	DT	VLSID	ECA		SEMINAR	
FRIDAY	VLSID	AIML	MCA (T)	SH		B1: AIML LAB (BRD + RS) B6 : 104 B2: VLSID LAB (TSK+VSR) B6 : 208	
SATURDAY							

Course Title	Course Code & Course Name	Course Faculty
DT	U24OE501A Design Thinking	Dr. V. Raju (VR)
LCS	U24EC502 Linear Control Systems	Dr. M. Raju (MR)
MCA	U24EC503 Microcontrollers and Applications	Sri. D. Santhosh Kumar (DSK)
VLSID	U24EC504 VLSI Design	Dr. T. Sunil Kumar (TSK)
AIML	U24EC505 Artificial Intelligence and Machine Learning	Dr. B. Ramadevi (BRD)
EITK	U24IK506A Essence of Indian Traditional Knowledge	Dr. M. Chandrashekar (MC)
TE	U24MH507 Technical English	Smt. M. Lakma Kumari (MLK)
SH	Strategic Hour: Navigating studies, carrier and beyond	Sri. S. Pradeep Kumar (SPK)
MCA LAB	U24EC508 Microcontrollers and Applications Laboratory	B1 & B2: Sri. D. Santhosh Kumar (DSK) / Dr. MD. Abdul Muqueem (MQ)
VLSID LAB	U24EC509 VLSI Design Laboratory	B1 & B2: Dr. T. Sunil Kumar (DSK) / Sri. V. Shobhan Reddy (VSR)
AIML LAB	U24EC510 Artificial Intelligence and Machine Learning Laboratory	B1 & B2: Dr. B. Ramadevi (BRD) / Dr. R.Srikanth (RS)
SEMINAR	U24EC511 Seminar	Dr. J. Sheshagiri Babu (JSB)
ECA	ECE Association	Sri A. Pavan (AP) / Dr. K. Sowjanya (KS) / Dr. MD. Abdul Muqueem (MQ) / Smt. E. Susmitha (ESU)
MYC	Meet Your Counsellor	


 Faculty I/c,
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 Head of the Department
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 Overall Timetables


 Dean, AA

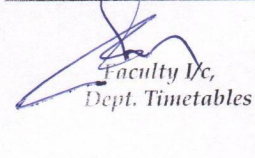

 Principal

ACADEMIC YEAR 2026-27: ODD SEMESTER	5ECE2
Scheme: URR24 V SEMESTER	B-VI-202
B.Tech Electronics & Communication Engineering (ECE)	W.e.f. 20.07.2026

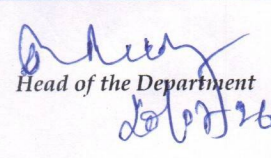
Class Teacher: Dr. Ch. Pavan Kumar (CHPK)

DAY/ TIME	9.30 - 10.30	10.30 - 11.30	11.30 - 12.30	12.30 - 01.30	L U N C H B R E A K	14.30 - 15.30	15.30 - 16.30
MONDAY	VLSID	AIML	MCA	EITK		DT	LCS (T)
TUESDAY	B2: MCA LAB (GRR+MQ) B1 : 204 B1: VLSID LAB (CHPK+ ESU) B6 : 208		LCS	DT		VLSID (T)	MCA (T)
WEDNESDAY	LCS	AIML	MCA	MYC		SEMINAR	
THURSDAY	B1: AIML LAB (AZS+DSK) B1:205 B2: VLSID LAB (CHPK+ESU) B6 : 208		EITK	ECA		TECHNICAL ENGLISH (B IV: 308 & 309	
FRIDAY	B1: MCA LAB (GRR+MQ) B1 : 204 B2: AIML LAB (AZS+DSK) B1 : 205		VLSID	SH		AIML (T)	DT (T)
SATURDAY							

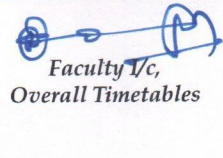
Course Title	Course Code & Course Name	Course Faculty
DT	U24OE501A Design Thinking	Dr. K. Kishore Kumar (KK)
LCS	U24EC502 Linear Control Systems	Dr. B. Dhanalaxmi (BD)
MCA	U24EC503 Microcontrollers and Applications	Dr. G. Raghothan Reddy (GRR)
VLSID	U24EC504 VLSI Design	Dr. Ch. Pavan Kumar (CHPK)
AIML	U24EC505 Artificial Intelligence and Machine Learning	Dr. A. Srinivas (AZS)
EITK	U24IK506A Essence of Indian Traditional Knowledge	Sri. S. Pradeep Kumar (SPK)
TE	U24MH507 Technical English	Smt M. Lakma Kumari (MLK)
SH	Strategic Hour: Navigating studies, carrier and beyond	Sri. S. Pradeep Kumar (SPK)
MCA LAB	U24EC508 Microcontrollers and Applications Laboratory	B1& B2: Dr. G. Raghothan Reddy (GRR)/ Dr. MD. Abdul Muqueem (MQ)
VLSID LAB	U24EC509 VLSI Design Laboratory	B1: Dr. Ch. Pavan Kumar (CHPK)/ Smt. E. Susmitha (ESU)
AIML LAB	U24EC510 Artificial Intelligence and Machine Learning Laboratory	B1 & B2: Dr. A. Srinivas (AZS) / Sri. D. Santhosh Kumar (DSK)
Seminar	U24EC511 Seminar	Dr. M. Chandrashekar (MC)
ECA	ECE Association	Sri A. Pavan (AP) / Dr. K. Sowjanya (KS) / Dr. MD. Abdul Muqueem (MQ) / Smt. E. Susmitha (ESU)
MYC	Meet Your Counsellor	



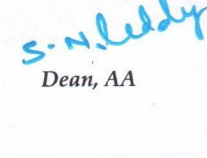
Faculty In-charge,
Dept. Timetables



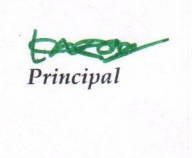
Head of the Department



Faculty In-charge,
Overall Timetables



Dean, AA



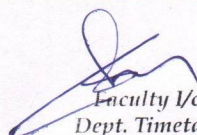
Principal

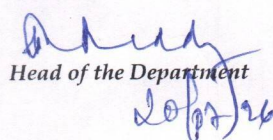
ACADEMIC YEAR 2026-27: ODD SEMESTER	5ECE3
Scheme: URR24 V SEMESTER	B-VI-203
B.Tech Electronics & Communication Engineering (ECE)	W.e.f. 20.07.2026

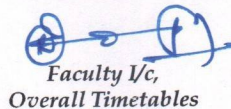
Class Teacher: Dr. MD. Abdul Muqueem (MQ)

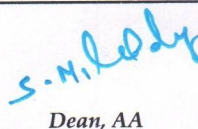
DAY / TIME	9.30 - 10.30	10.30 - 11.30	11.30 - 12.30	12.30 - 01.30	L U N C H B R E A K	14.30 - 15.30	15.30 - 16.30
MONDAY	B1: VLSI LAB (AP+SPK) B6 : 208 B2: AIML LAB (DV+BD) B1 : 205	DT	VLSID			LCS	MCA
TUESDAY	AIML	DT	VLSID (T)	LCS (T)		B2: VLSI LAB (AP+SPK) B6 : 208 B1: MCA LAB (SPG+MQ) B1 : 204	
WEDNESDAY	VLSID	AIML	EITK	MYC		TECHNICAL ENGLISH (B IV: 308 & 309)	
THURSDAY	LCS	EITK	MCA (T)	ECA		SEMINAR	
FRIDAY	MCA	DT (T)	AIML (T)	SH		B1: AIML LAB (DV+BD) B1 : 205 B2: MCA LAB (SPG+MQ) B1 : 204	
SATURDAY							

Course Title	Course Code & Course Name	Course Faculty
DT	U24OE501A Design Thinking	Dr. V. Raju (VR)
LCS	U24EC502 Linear Control Systems	Dr. MD. Abdul Muqueem (MQ)
MCA	U24EC503 Microcontrollers and Applications	Dr. S. P. Girija (SPG)
VLSID	U24EC504 VLSI Design	Sri. A. Pavan (AP)
AIML	U24EC505 Artificial Intelligence and Machine Learning	Dr. D. Venu (DV)
EITK	U24IK506A Essence of Indian Traditional Knowledge	Dr. B. Dhanalaxmi (BD)
TE	U24MH507 Technical English	Smt. T. Veena Raj (TV)
SH	Strategic Hour: Navigating studies, carrier and beyond	Sri. S. Pradeep Kumar (SPK)
MCA LAB	U24EC508 Microcontrollers and Applications Laboratory	B1 & B2: Dr. S. P. Girija (SPG) / Dr. MD. Abdul Muqueem (MQ)
VLSID LAB	U24EC509 VLSI Design Laboratory	B1 & B2: Sri. A. Pavan (AP) / Sri. S. Pradeep Kumar (SPK)
AIML LAB	U24EC510 Artificial Intelligence and Machine Learning Laboratory	B1 & B2: Dr. D. Venu (DV) / Dr. B. Dhanalaxmi (BD)
Seminar	U24EC511 Seminar	Dr. B. Dhanalaxmi (BD)
ECA	ECE Association	Sri A. Pavan (AP) / Dr. K. Sowjanya (KS) / Dr. MD: Abdul Muqueem (MQ) / Smt. E. Susmitha (ESU)
MYC	Meet Your Counsellor	


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 Dept. Timetables


 Head of the Department
 20/07/26


 Faculty I/c,
 Overall Timetables


 Dean, AA

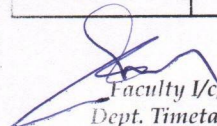

 Principal

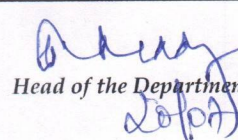
ACADEMIC YEAR 2026-27: ODD SEMESTER	7ECE1
Scheme: URR18R22 VII SEMESTER	B-VI-204/203/201
B.Tech Electronics & Communication Engineering (ECE)	w.e.f.20.07.2026

Class Teacher: Dr. T. Sunil Kumar (TSK)

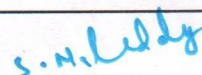
DAY / TIME	9.30 - 10.30	10.30 - 11.30	11.30 - 12.30	12.30 - 01.30	L U N C H B R E A K	14.30 - 15.30	15.30 - 16.30
MONDAY	DM B6: 204	DSE B6: 204 / DSD B6 : 203 / RTES B6 : 201	VLSI PD B6: 204 / DIP B6 : 103	IE		Major Project Work Phase - I	
TUESDAY	MEA B6: 204	DM B6: 204	VLSI PD: B6: 105 / DIP B6 : 204	IE		B1: VLSI LAB (TSK+VSR) (B1:219) (ECAD LAB)/ B2: PPIOT LAB (DSK+EJ) B1: 205	
WEDNESDAY	DSE B6: 204 / DSD B6 : 201 / RTES B6 : 102	MEA B6: 204	DM B6: 204	MYC		B1: PPIOT LAB (DSK+EJ) B1 : 205 / B2: VLSI LAB (TSK+PC) (B1:219) (ECAD LAB)	
THURSDAY	VLSI PD B6: 204 / DIP B6 : 202	MEA B6: 204	DSE B6: 204/ DSD: B IV 305 / RTES: B6: 308	ECA		Major Project Work Phase - I	
FRIDAY	Major Project Work Phase - I					Major Project Work Phase - I	
SATURDAY							

Course Title	Course Code & Course Name	Course Faculty
DM	U18OE701A: Disaster Management	Sri. B. Komuraiah (BK)
DSE	U18EC702A: Data Science Engineering	Dr. V. Venkateshwar Reddy (VVR)
RTES	U18EC702B: Real-Time Embedded Systems	Sri. P. Ramchandrar Rao (PRR)
DSD	U18EC702D: Digital System Design	Mr. Syed Zaheeruddin (SYZ)
VLSI PD	U18EC703B: VLSI Physical Design	Dr. T. Sunil Kumar (TSK)
DIP	U18EC703C: Digital Image Processing	Dr. S. P. Girija (SPG)
VLSI LAB	U18EC706 VLSI Lab	B1: Dr. T. Sunil Kumar (TSK) / Sri. V. Shobhan Reddy (VSR) B2: Dr. T. Sunil Kumar (TSK)/ Sri. P. Chiranjeevi (PC)
PPIOT LAB	U18EC710** Python Programming & IoT Lab	B1&B2: Sri. D. Santhosh Kumar (DSK) / Ms. E. Jawali (EJ)
PROJ	U18EC707 Major Project Phase - I	Dr. D. Venu (DV)
IE	U18EC708 Internship Evaluation	Sri. P. Ramchandrar Rao (PRR)
MEA	U18MH711# Management Economics & Accountancy	Dr. P. Surender (PS)
ECA	ECE Association	Sri A. Pavan (AP) / Dr. K. Sowjanya (KS) / Dr. MD. Abdul Muqueem (MQ) / Smt. E. Susmitha (ESU)
MYC	Meet Your Counsellor	


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 Dept. Timetables


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 20/07/26


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 Overall Timetables


 Dean, AA

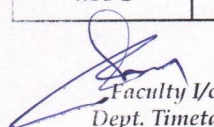

 Principal

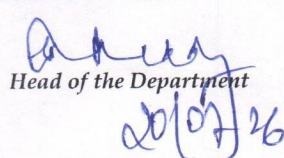
ACADEMIC YEAR 2026-27: ODD SEMESTER	7ECE2
Scheme: URR18R22 VII SEMESTER	B-VI-203/201/202
B.Tech Electronics & Communication Engineering (ECE)	w.e.f.20.07.2026

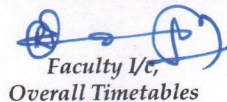
Class Teacher: Sri. P. Yugandar (PY)

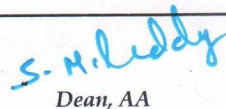
DAY / TIME	9.30 - 10.30	10.30 - 11.30	11.30 - 12.30	12.30 - 01.30	L U N C H B R E A K	14.30 - 15.30	15.30 - 16.30	
MONDAY	MEA B6: 203	DSE B6: 105 / DSD B6 : 203 / RTES B6 : 201	VLSI PD B6: 105 / DIP B6 : 103	IE		B1: VLSI LAB (EJ+JSB) (B1:219) (ECAD LAB) / B2: PPIOT LAB (PY+AZS) B1 : 205		
TUESDAY	DM B6: 202	MEA B6: 202	VLSI PD B1: 205 / DIP B6 : 204	IE		Major Project Work Phase - I		
WEDNESDAY	DSE B1: 205 / DSD B6: 201/ RTES B6 : 102	MEA B6 : 201	DM B IV 305	MYC		Major Project Work Phase - I		
THURSDAY	VLSI PD B1: 204 / DIP B6 : 202	DM B6: 202	DSE B1: 204/ DSD: B IV 305 / RTES B6: 308	ECA		B1: PPIOT LAB (PY+AZS+GRR) B1 : 205 / B2: VLSI LAB (EJ+JSB) (B1:219) (ECAD LAB)		
FRIDAY	Major Project Work Phase - I							
SATURDAY								

Course Title	Course Code & Course Name	Course Faculty
DM	U18OE701A: Disaster Management	Sri.A. Pavan (AP)
DSE	U18EC702A: Data Science Engineering	Sri. P. Yugandar (PY)
RTES	U18EC702B: Real-Time Embedded Systems	Sri. P. Ramchandrarao (PRR)
DSD	U18EC702D: Digital System Design	Sri. Syed Zaheeruddin (SYZ)
VLSI PD	U18EC703B: VLSI Physical Design	Sri. P. Chiranjeevi (PC)
DIP	U18EC703C: Digital Image Processing	Dr. S. P. Girija (SPG)
VLSI LAB	U18EC706 VLSI Lab	B1&B2: : Ms. E. Jawali (EJ) / Dr. J. Sheshagiri Babu (JSB)
PPIOT LAB	U18EC710 Python Programming & IoT Lab	B1&B2: Sri. P. Yugandar (PY) / Dr. A. Srinivas (AZS)
PROJ	U18EC707 Major Project Phase - I	Dr. V. Raju (VR)
IE	U18EC708 Internship Evaluation	Sri. V. Shobhan Reddy (VSR)
MEA	U18MH711 Management Economics & Accountancy	Dr. Sunitha Chakravarthy (SC)
ECA	ECE Association	Sri A. Pavan (AP) / Dr. K. Sowjanya (KS) / Dr. MD. Abdul Muqueem (MQ) / Smt. E. Susmitha (ESU)
MYC	Meet Your Counsellor	


 Faculty I/c,
 Dept. Timetables


 Head of the Department
 20/07/26


 Faculty I/c,
 Overall Timetables


 Dean, AA

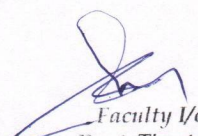

 Principal

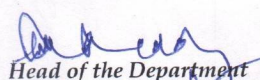
ACADEMIC YEAR 2026-27: ODD SEMESTER	7ECE3
Scheme: URR18R22 VII SEMESTER	B-VI-201/203/204
B.Tech Electronics & Communication Engineering (ECE)	w.e.f.20.07.2026

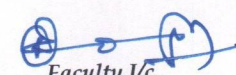
Class Teacher: Ms. E. Jawali (EJ)

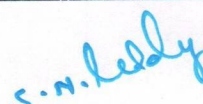
DAY / TIME	9.30 - 10.30	10.30 - 11.30	11.30 - 12.30	12.30 - 01.30	L U N C H B R E A K	14.30 - 15.30	15.30 - 16.30
MONDAY	MEA B6 : 201	DSE B6: 204 / DSD B6 : 203 / RTESB6 : 201	VLSI PD B6: 104 / DIP B6 : 103	IE		B1: VLSI LAB (VR+MR) B1 : 204/ B2: PPIOT LAB (CHPK+DV) B6 : 104	
TUESDAY	DM B6 : 103	MEA B6 : 103	VLSI PD B6: 104/ DIP B6 : 204	IE		Major Project Work Phase - I	
WEDNESDAY	DSE B6: 204 / DSD B6 : 201 / RTES B6 : 102	MEA B6 : 102	DM B IV 306A	MYC		Major Project Work Phase - I	
THURSDAY	VLSI PD B6: 102 / DIP B6 : 202	DM B6: 102	DSE B6: 204 / DSD: B IV 305 / RTES B6: 308	ECA		B1: VLSI LAB (VR+MR) B1 : 204 / B2: PPIOT LAB (CHPK+DV) B6 : 104	
FRIDAY	Major Project Work Phase - I					Major Project Work Phase - I	
SATURDAY							

Course Title	Course Code & Course Name	Course Faculty
DM	U18OE701A: Disaster Management	Dr. R. Srikanth (RS)
DSE	U18EC702A: Data Science Engineering	Dr. V. Venkateshwar Reddy (VVR)
RTES	U18EC702B: Real-Time Embedded Systems	Sri. P. Ramchandrar Rao (PRR)
DSD	U18EC702D: Digital System Design	Sri. Syed Zaheeruddin (SYZ)
VLSI PD	U18EC703B: VLSI Physical Design	Ms. E. Jawali (EJ)
DIP	U18EC703C: Digital Image Processing	Dr. S. P. Girija (SPG)
VLSI LAB	U18EC706 VLSI Lab	Dr. V. Raju (VR) / Dr. M. Raju (MR)
PPIOT LAB	U18EC710 Python Programming & IoT Lab	Dr. Ch. Pavan Kumar (CHPK) / Dr. D. Venu (DV) / Dr. G. Raghitham Reddy
PROJ	U18EC707 Major Project Phase - I	Dr. R. Srikanth (RS)
IE	U18EC708 Internship Evaluation	Ms. E. Jawali (EJ)
MEA	U18MH711# Management Economics & Accountancy	Dr. K. Jaipal (KJ)
ECA	ECE Association	Sri A. Pavan (AP) / Dr. K. Sowjanya (KS) / Dr. MD. Abdul Muqueem (MQ) / Smt. E. Susmitha (ESU)
MYC	Meet Your Counsellor	


 Faculty I/c,
 Dept. Timetables


 Head of the Department
 20/7/26


 Faculty I/c,
 Overall Timetables


 Dean, AA


 Principal