

ACADEMIC YEAR 2026-27: ODD SEMESTER	3ECI
Scheme: URR24-R25 III SEMESTER	B-VI-205
B.Tech Electronics Communication and Instrumentation Engineering (ECI)	w.e.f.20.07.2026

Class Teacher: Smt. B. Smitha

DAY/TIME	09:30-10:30	10:30-11:30	11:30-12:30	12:30-01:30		02:30-03:30	03:30-04:30
MONDAY	MFSP	DCLD	AIC&A	S&A	L U N C H	B1: Sensors & Actuators Lab (MRR+GR) (B1-220- SI&AI-Lab) / B2: Analog Integrated Circuits and Applications Lab (KSC+BS) (B1-218- BE & IC Lab)	
TUESDAY	DCLD	OOPJ	MFSP	ECIA		B1: Analog Integrated Circuits and Applications Lab (KSC+BS) (B1-218- BE & IC Lab) / B2: Sensors & Actuators Lab (MRR+GR) (B1-220- SI&AI-Lab)	
WEDNESDAY	OOP through Java Lab (BJ+BS+KSJ) (B1-220-SI&AI Lab)		S&A	AIC&A(T)		OOPJ(T)	DCLD(T)
THURSDAY	OOPJ	AIC&A	MFSP	MYC		Programming Skill Development Lab-2 (RND+BJ) (B1-220-SI&AI Lab)	
FRIDAY	S&A	AIC&A	DCLD	OOPJ		S&A(T)	MFSP(T)
SATURDAY	-	-	-	-			

Course title in short	Course code & course name		Name of the course faculty
MSFP	U24MH301C	Mathematical foundations for Signal Processing	Smt. B. Smitha (BS)
S&A	U24CI302	Sensors and Actuators	Dr. M. Raghu Ram (MRR)
AIC&A	U24CI303	Analog Integrated Circuits and Applications	Prof. K. Venu Madhav (KVM)
DCLD	U24CI304	Digital Circuits and Logic Design	Dr. R. Nirmala Devi (RND)
OOPJ	U24CI305	OOP through Java	Dr. B. Jeevan (BJ)
S&A Lab	U24CI306	Sensors and Actuators Lab	Dr. M. Raghu Ram (MRR), Sri G. Raju (GR)
PSDLab-2	U24SE307	Programming Skill Development Lab-2	Dr. R. Nirmala Devi (RND), Dr. B. Jeevan (BJ)
AIC&A Lab	U24CI308	Analog Integrated Circuits and Applications Lab	Dr. K. Srinivas (KSC), Smt. B. Smitha (BS)
OOPJ Lab	U24CI309	OOP through Java Lab	Dr. B. Jeevan(BJ), Smt. B. Smitha (BS), Dr. K. Shailaja (KSJ)
MYC	MEET YOUR COUNSELLOR		
ECIA	Electronics Communication & Instrumentation Engineering Association		Dr. B. Venu Maheswar Rao (BVM)

Smt. B. Smitha
 Faculty I/c,
 Dept. Timetables

[Signature]
 Faculty I/c,
 Overall Timetables

[Signature]
 Head of the Department

[Signature]
 Dean, AA

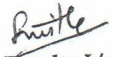
[Signature]
 Principal

ACADEMIC YEAR 2026-27: ODD SEMESTER	5ECI
Scheme: URR24- V SEMESTER	B-VI-206
B.Tech Electronics Communication and Instrumentation Engineering (ECI)	w.e.f.20.07.2026

Class Teacher: Dr. K. Shailaja

DAY/TIME	9.30-10.30	10.30-11.30	11.30-12.30	12.30-1.30		2.30-3.30	3.30-4.30
MONDAY	MCES	IoT	LCS	DT		AIML	EITK
TUESDAY	B1: Microcontrollers and Embedded Systems Lab (KVM+KSJ) (B-I-201-MPMC Lab) / B2: AIML Applications Lab (BSK+BVM) (B-I-220-SI&AI LAB)		LCS(T) B1_220 SI&AI LAB	ECIA	L U N C H	DT(T)	AIML(T)
WEDNESDAY	IoT	MCES	AIML	LCS		B1: AIML Applications Lab (BSK+BVM) (B-I-220-SI&AI LAB)/ B2: Microcontrollers and Embedded Systems Lab (KVM+KSJ) (B-I-201-MPMC Lab)	
THURSDAY	LCS	IoT	EITK	MYC		MCES	DT
FRIDAY	DT	AIML	IoT(T)	MCES(T)		Technical English (DVN) (B-IV-308 &309)	
SATURDAY	SEMINAR (BVM+GR) (B1-217-VI LAB)		-	-			

Course title in short		Course code & course name	Name of the course faculty
DT	U24OE501A	Design Thinking	Sri Druti Sundar Das (DSD)
IoT	U18CI502	Internet of things	Dr. K. Srinivas (KSC)
MCES	U24CI503	Microcontrollers and Embedded Systems	Prof. K. Venu Madhav (KVM)
AIML	U24CI504	Artificial Intelligence and Machine Learning	Sri B. Shashikanth(BSK)
LCS	U24CI505	Linear Control Systems	Dr. K. Shailaja (KSJ)
EITK	U24IK506A	Essence of Indian Traditional Knowledge	Dr. B. Jeevan (BJ)
TE	U24MH507	Technical English	Dr. D. Vidyanath (DVN)
MCES Lab	U24CI508	Microcontrollers and Embedded Systems Lab	Prof. K. Venu Madhav (KVM), Dr. K. Shailaja (KSJ)
AIML	U24CI509	AIML Applications Lab	Sri B. Shashikanth(BSK), Dr. B. Venu Maheswar Rao (BVM)
SEMINAR	U24CI510	Seminar	Dr. R. Nirmala Devi(RND), Dr. B. Venu Maheswar Rao (BVM), Sri G. Raju (GR)
MYC	MEET YOUR COUNSELLOR		
ECIA	Electronics Communication & Instrumentation Engineering Association		Dr. B. Venu Maheswar(BVM)


 Faculty I/c,
 Dept. Timetables


 Faculty I/c,
 Overall Timetables


 Head of the Department


 Dean, AA

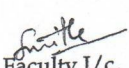

 Principal

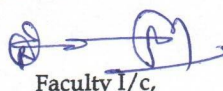
ACADEMIC YEAR 2026-27: ODD SEMESTER	7ECI
Scheme: URR18-R22 VII SEMESTER	w.e.f.20.07.2026
B.Tech Electronics Communication and Instrumentation Engineering (ECI)	

Class Teacher: Sri G. Raju

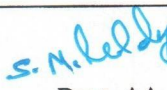
DAY/TIME	09:30-10:30	10:30-11:30	11:30-12:30	12:30-01:30		02:30-03:30	03:30-04:30
MONDAY	B1: Industrial Process Control Lab (BSK+MRR) (B1-104C- IPC Lab)/ B2: Digital Design Lab (OA+BJ) (B1-219- E-CAD Lab)		SC(T) B1_220_SI&AI LAB	DM(T) B1_220_SI&AI LAB	L U N C H	T&P Practice Session (TPPS) (BSK+BJ+KSJ) B1_VI LAB/B2_MPMC LAB)	
TUESDAY	SC B6-206	VLSID B6-206	IPC B6-206	ECIA		DM B6-205	VLSID(T) B6-205
WEDNESDAY	IPC B6-205	SC B6-205	DM B1_220_SI&AI LAB	VLSID B1_220_SI&AI LAB		MPW	
THURSDAY	B1: Digital Design Lab (OA+RND) (B1-219- E-CAD Lab) / B2: Industrial Process Control Lab (BSK+MRR) (B1-104C- IPC Lab)		IPC(T) B1_220_SI&AI LAB	MYC		Internship Evaluation (B1-219 -E-CAD LAB)	
FRIDAY	MPW	MPW	MPW	MPW		MPW	MPW
SATURDAY	-	-	-	-		-	-

Course title in short	Course code & course name		Name of the course faculty
PM (OE -III)	U18OE701A	Disaster Management	Dr. R. Nirmala Devi (RND)
SC (PE - III)	U18CI702D	Satellite communications	Sri G. Raju (GR)
VLSID (PE-IV)	U18CI703D	VLSI System Design	Dr. O. Anjaneyulu(OA)
IPC	U18CI 704	Industrial Process Control	Sri B. Shashikanth (BSK)
IPC LAB	U18CI 705	Industrial Process Control Laboratory	Sri B. Shashikanth (BSK), Dr. M. Raghu Ram (MRR)
DDLAB	U18CI 709	Digital Design Laboratory	Dr. O. Anjaneyulu(OA), Dr. R. Nirmala Devi (RND), Dr. B. Jeevan (BJ)
MPW	U18CI 707	Major Project Phase - I	Prof. K. Venu Madhav (KVM), Dr. K. Srinivas (KSC)
IE	U18CI 708	Internship Evaluation	Dr. O. Anjaneyulu(OA), Sri B. Shashikanth(BSK), Dr. K. Shailaja (KSJ)
MYC	MEET YOUR COUNSELLOR		
TPPS	T&P Practice Session		Sri B. Shashikanth (BSK), Dr. B. Jeevan (BJ), Dr. K. Shailaja(KSJ)
ECIA	Electronics Communication & Instrumentation Engineering Association		Dr. B. Venu Maheswar Rao(BVM)


 Faculty I/c,
 Dept. Timetables


 Faculty I/c,
 Overall Timetables


 Head of the Department


 Dean, AA


 Principal